



PATENT
8040-1022

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Hiroataka NAKANO et al.

Serial No. 09/855,723

Filed May 16, 2001

SYSTEM OF MANUFACTURING
SEMICONDUCTOR INTEGRATED CIRCUIT

Conf. 9420

Group 2825

Examiner M. Dimyan

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 4, 2004

Sir:

In response to the Official Action mailed November 7, 2003, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 6 of this paper.

Remarks begin on page 7 of this paper.

An **Appendix** is attached following the signature page of this paper.